

	Subclass	Class	ISSUE CLASSIFICATION
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UTILITY SERIAL NUMBER	005381	PATENT DATE	PATENT NUMBER		
SERIAL NUMBER	08/005,381	FILING DATE	CLASS 315	SUBCLASS 209 R	GROUP ART UNIT 2502
EXAMINER MIS					

APPLICANTS
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CONTINUING DATA**

VERIFIED THIS APPLN IS A CON OF 07/840,528 02/25/92 PAT 5,189,342
Dm
 WHICH IS A CON OF 07/46/646,497 05/04/84 PAT 4,540,763
 WHICH IS A CON OF 07/107,795 10/13/87 ABN
 WHICH IS A CIP OF 06/658,423 10/05/84 ABN
 WHICH IS A CIP OF 06/555,426 11/23/83 ABN
 WHICH IS A CON OF 06/178,107 08/14/80 ABN
 WHICH IS A CIP OF 05/973,741 12/28/78 ABN
 WHICH IS A CIP OF 05/890,586 03/20/78 PAT 4,184,128

FOREIGN/PCT APPLICATIONS***

VERIFIED *NONE*

NOTE - DISCLAIMER

The term of this patent

subsequent to 2/23/10

has been disclaimed

FOREIGN FILING LICENSE GRANTED 02/12/93

Foreign priority claimed 35 USC 119 conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	AS FILED →	STATE OR COUNTRY IL	SHEETS DRWGS. 1	TOTAL CLAIMS 12	INDEP. CLAIMS 3	FILING FEE RECEIVED \$710.00	ATTORNEY'S DOCKET NO.
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Verified and Acknowledged *Examiner's initials*

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TITLE ELECTRONIC BALLAST WITH PARALLEL TUNED OUTPUT CIRCUIT HAVING SERIES-CONNECTED TRANSISTORS AND SINE-SOIDSAL OUTPUT VOLTAGE

U.S. DEPT. OF COMM-Pat. & TM Office - PTO-436 (rev. 10-78)

PARTS OF APPLICATION FILED SEPARATELY		<i>CNE</i>		Applications Examiner
NOTICE OF ALLOWANCE MAILED				CLAIMS ALLOWED
				Total Claims <i>20</i>
				Print Claim
ISSUE FEE		Assistant Examiner		DRAWING
Amount Due	Date Paid			Sheets Drwg. Figs. Drwg. Print Fig.
				ISSUE BATCH NUMBER
Primary Examiner				
PREPARED FOR ISSUE				
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